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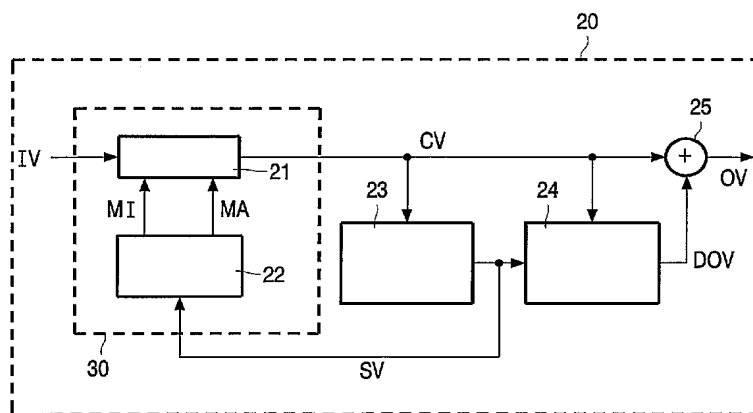
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(54) Title: OVERDRIVING A PIXEL OF A MATRIX DISPLAY



(57) **Abstract:** An overdrive circuit (20) for a display panel (1) comprising a pixel (10) having inertia further comprises a table look up circuit (24) or a function circuit (24) which receives a start value (SV) and a desired value (DV) being either an input value (IV) indicating an image to be displayed, or a clipped value (CV) and which supplies an overdrive value (OV) to the pixel (10). A substituting circuit (30) substitutes the input value (IV) by a reachable response within one predetermined period (Tf) starting from the start value (SV) to obtain the clipped value (CV). A memory (23) receives the clipped value (CV) to supply the start value (SV) which is the clipped value (CV) delayed over the one predetermined period (Tf). The substituting circuit (30) comprises a table look up circuit or a function circuit (22) which indicates for the start value (SV) a corresponding minimum value (MI) reached from the start value (SV) within one predetermined period (Tf) if a minimum drive value (MID) is applied to the pixel (10), and a corresponding maximum value (MS) reached from the start value (SV) within one predetermined period (Tf) if a maximum drive value (MAD) is applied to the pixel (10).

WO 2005/096262 A1



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